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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Mark G. Johnson, Thomas H. Lee & James M. Cleeves

Title: INTEGRATED CIRCUIT INCORPORATING THREE-DIMENSIONAL
MEMORY ARRAY WITH DUAL OPPOSING DECODER
ARRANGEMENT

Application No.: 10/774,818

Filed: February 9, 2004

Examiner: Thong Q. Le

Group Art Unit: 2827

Atty. Docket No.: 023-0025

Confirmation No.: 9232

March 8, 2006

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450**RESPONSE TO NON-FINAL OFFICE ACTION**

This paper is responsive to the Non-Final Office Action mailed on December 8, 2005, having a shortened statutory period for response set to expire March 8, 2006. In light of the Amendments and/or Remarks herein, further examination is requested.

Amendments to the Claims begin on page 2.

Remarks begin on page 9.

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